



1449		ATTORNEY DOCKET NO. AUS920030553US1		SERIAL NO. 10/757,212	
LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)					
APPLICANT DeWitt, Jr. et al.					
FILING DATE January 14, 2004				GROUP ART UNIT 2188 2185	
RELATED PATENT APPLICATIONS					
EXAMINER INITIAL		APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
A.S.	AA	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
A.S.	AB	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
A.S.	AC	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
A.S.	AD	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
A.S.	AE	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
A.S.	AF	10/675,751 / AUS920030482US1	DeWitt, Jr. et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
A.S.	AG	10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
A.S.	AH	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
A.S.	AI	10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Execution on Specified Instructions	Sep. 30, 2003
A.S.	AJ	10/675,783 / AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications	Sep. 30, 2003
A.S.	AK	10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003
A.S.	AL	10/757,250 / AUS920030488US1	Levine et al.	Method and Apparatus for Maintaining Performance Monitoring Structures in a Page Table for use in Monitoring Performance of a Computer Program	Jan. 14, 2004
A.S.	AM	10/757,186 / AUS920030540US1	DeWitt, Jr. et al.	Method and Apparatus for Qualifying Collection of Performance Monitoring Events by Types of Interrupt when Interrupt Occurs	Jan. 14, 2004
A.S.	AN	10/757,198 / AUS920030541US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Interrupts by Type	Jan. 14, 2004
A.S.	AO	10/757,248 / AUS920030544US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses to Identify Hot Spots	Jan. 14, 2004
A.S.	AP	10/757,269 / AUS920030549US1	DeWitt, Jr. et al.	Method and Apparatus for Autonomically Initiating Measurement of Secondary Metrics Based on Hardware Counter Values for Primary Metrics	Jan. 14, 2004
A.S.	AQ	10/757,237 / AUS920030550US1	DeWitt, Jr. et al.	Autonomic Method and Apparatus for Counting Branch Instructions to Improve Branch Predictions	Jan. 14, 2004
A.S.	AR	10/757,197 / AUS920030556US1	DeWitt, Jr. et al.	Method and Apparatus for Optimizing Code Execution Using Annotated Trace Information having Performance Indicator and Counter Information	Jan. 14, 2004
DATE CONSIDERED 2/22/06			EXAMINER Arden Sarka [Signature]		
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					

Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT <i>(Use several sheets if necessary)</i>			ATTORNEY DOCKET NO. AUS920030553US1		SERIAL NO. Not Assigned <i>10/757, 212</i>	
APPLICANT DeWitt, Jr. et al.			FILING DATE <i>Jan. 14, 2004</i>		GROUP ART UNIT Not Assigned <i>2185</i>	
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/SUBCLASS	FILING DATE	
<i>AS</i>	AA 5,103,394	Apr. 7, 1992	Blasciak	395/575	Dec. 21, 1989	
<i>AS</i>	AB 6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999	
<i>AS</i>	AC 6,480,938 B2	Nov. 12, 2002	Vondran, Jr.	711/125	Dec. 15, 2000	
<i>AS</i>	AD 6,430,741 B1	Aug. 6, 2002	Mattson, Jr. et al.	717/154	Feb. 26, 1999	
<i>AS</i>	AE 6,189,141 B1	Feb. 13, 2001	Benitez et al.	717/4	May 4, 1998	
<i>AS</i>	AF 5,930,508	Jul. 27, 1999	Faraboschi et al.	395/706	Jun. 9, 1997	
<i>AS</i>	AG 6,351,844 B1	Feb. 26, 2002	Bala	717/4	Nov. 5, 1998	
<i>AS</i>	AH 6,324,689 B1	Nov. 27, 2001	Lowney et al.	717/9	Sep. 30, 1998	
<i>AS</i>	AI 6,442,585 B1	Aug. 27, 2002	Dean et al.	709/108	Nov. 26, 1997	
<i>AS</i>	AJ 5,774,724	Jun. 30, 1998	Heisch	395/704	Nov. 20, 1995	
<i>AS</i>	AK 5,987,250	Nov. 16, 1999	Subrahmanyam	395/704	Aug. 21, 1997	
<i>AS</i>	AL 6,192,513 B1	Feb. 20, 2001	Subrahmanyam	717/5	Nov. 2, 1998	
<i>AS</i>	AM 5,691,920	Nov. 25, 1997	Levine et al.	364/551.01	Oct. 2, 1995	
<i>AS</i>	AN 6,223,338 B1	Apr. 24, 2001	Smolders	717/4	Sep. 30, 1998	
<i>AS</i>	AO 6,101,524	Aug. 8, 2000	Choi et al.	709/102	Oct. 23, 1997	
<i>AS</i>	AP 6,256,775 B1	Jul. 3, 2001	Flynn	717/4	Dec. 11, 1997	
<i>AS</i>	AQ 6,446,029 B1	Sep. 3, 2002	Davidson et al.	702/186	Jun. 30, 1999	
<i>AS</i>	AR 6,134,676	Oct. 17, 2000	VanHuben et al.	714/39	Apr. 30, 1998	
<i>AS</i>	AS 5,937,437	Aug. 10, 1999	Roth et al.	711/202	Oct. 28, 1996	
<i>AS</i>	AT 6,243,804 B1	Jun. 5, 2001	Cheng	712/228	Jul. 22, 1998	
<i>AS</i>	AU 4,291,371	Sep. 22, 1981	Holtey	364/200	Jan. 2, 1979	
<i>AS</i>	AV 5,938,778	Aug. 17, 1999	John, Jr. et al.	714/45	Nov. 10, 1997	
<i>AS</i>	AW 6,286,132 B1	Sep. 4, 2001	Tanaka et al.	717/4	Jan. 7, 1999	
<i>AS</i>	AX 6,206,584 B1	Mar. 27, 2001	Hastings	395/183.11	May 31, 1995	
<i>AS</i>	AY 6,374,364 B1	Apr. 16, 2002	McElroy et al.	714/10	Jan. 19, 1999	
<i>AS</i>	AZ 6,070,009	May 30, 2000	Dean et al.	395/704	Nov. 26, 1997	
<i>AS</i>	BA 5,966,537	Oct. 12, 1999	Ravichandran	395/709	May 28, 1997	
DATE CONSIDERED <i>2/22/06</i>			EXAMINER <i>Arpan Sarla</i>			
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U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
A.S.	BB	2002/0129309	Sep. 12, 2002	Floyd et al.	714/724 Dec. 18, 2000
A.S.	BC	2001/0032305	Oct. 18, 2001	Barry	712/34 Feb. 23, 2001
A.S.	BD	2002/0199179	Dec. 26, 2002	Lavery et al.	717/158 Jun. 21, 2001
A.S.	BE	2002/0124237	Sep. 5, 2002	Sprunt et al.	717/127 Dec. 29, 2000
A.S.	BF	2002/0147965	Oct. 10, 2002	Swaine et al.	717/124 Feb. 1, 2001
A.S.	BG	2002/0019976	Feb. 14, 2002	Patel et al.	717/137 May 25, 2001
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EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
A.S.	BH	JP2000029731	Dec. 8, 1999	Japan	G06F 9/38 X
A.S.	BI	JP2000347863	Dec. 15, 2000	Japan	G06F 9/38 X
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	BJ	Kikuchi, "Parallelization Assist System", Johe Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.			
A.S.	BK	Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.			
A.S.	BL	Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.			
A.S.	BM	Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.			
A.S.	BN	Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Process r", IEEE International Performance, Computing, and Communications Conference, 19 th , Phoenix, Feb. 20-22, 2000, pp. 516-524.			
A.S.	BO	IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.			
RELATED PATENT APPLICATIONS					
EXAMINER INITIAL	APPLICATION NO / ATTY. DOCKET NO.	APPLICANT	TITLE		FILING DATE
A.S.	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug		Nov. 4, 1999
A.S.	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System		Oct. 2, 1995
DATE CONSIDERED 2/22/06			EXAMINER Arpen Savla Japan		
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